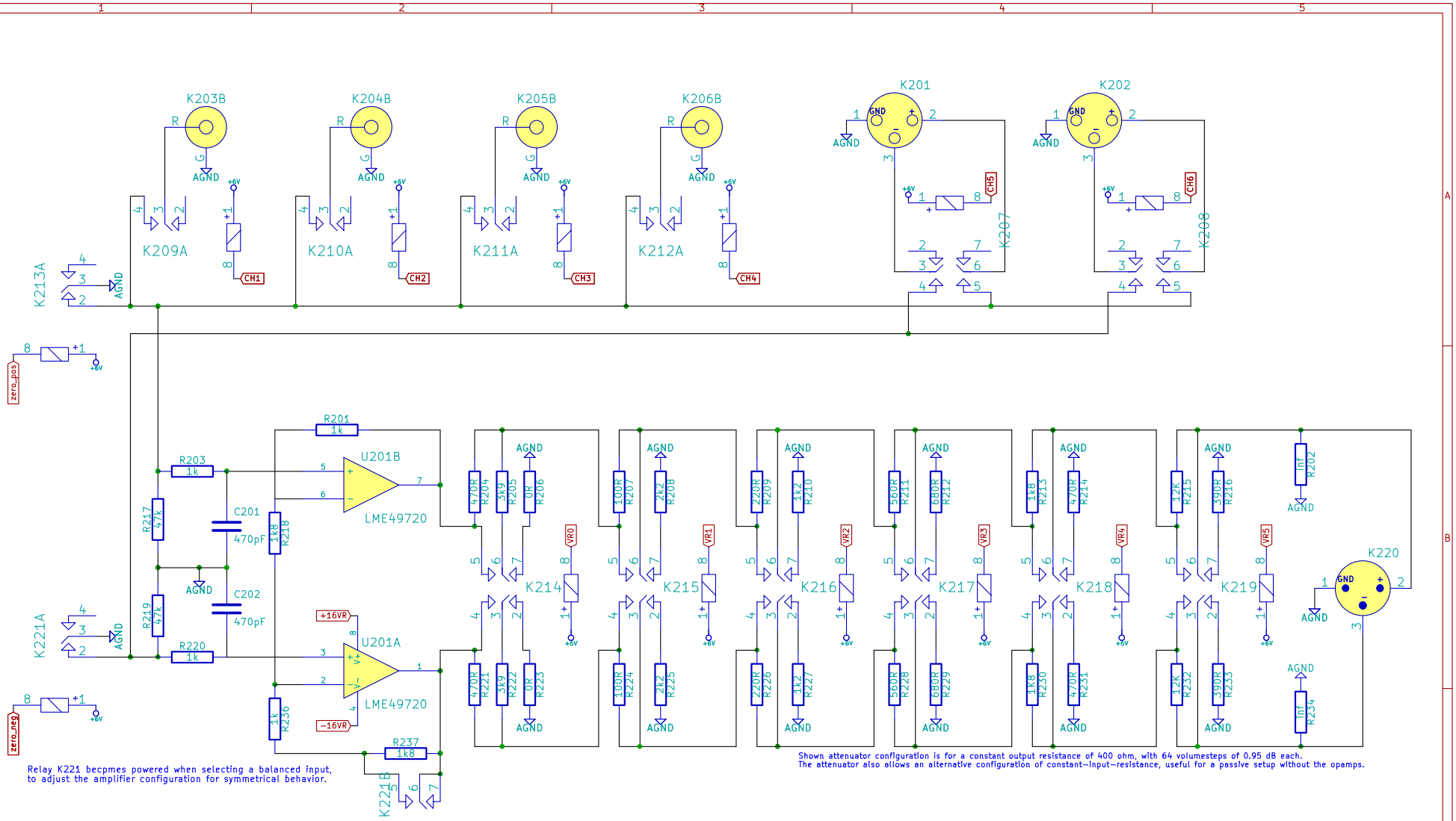
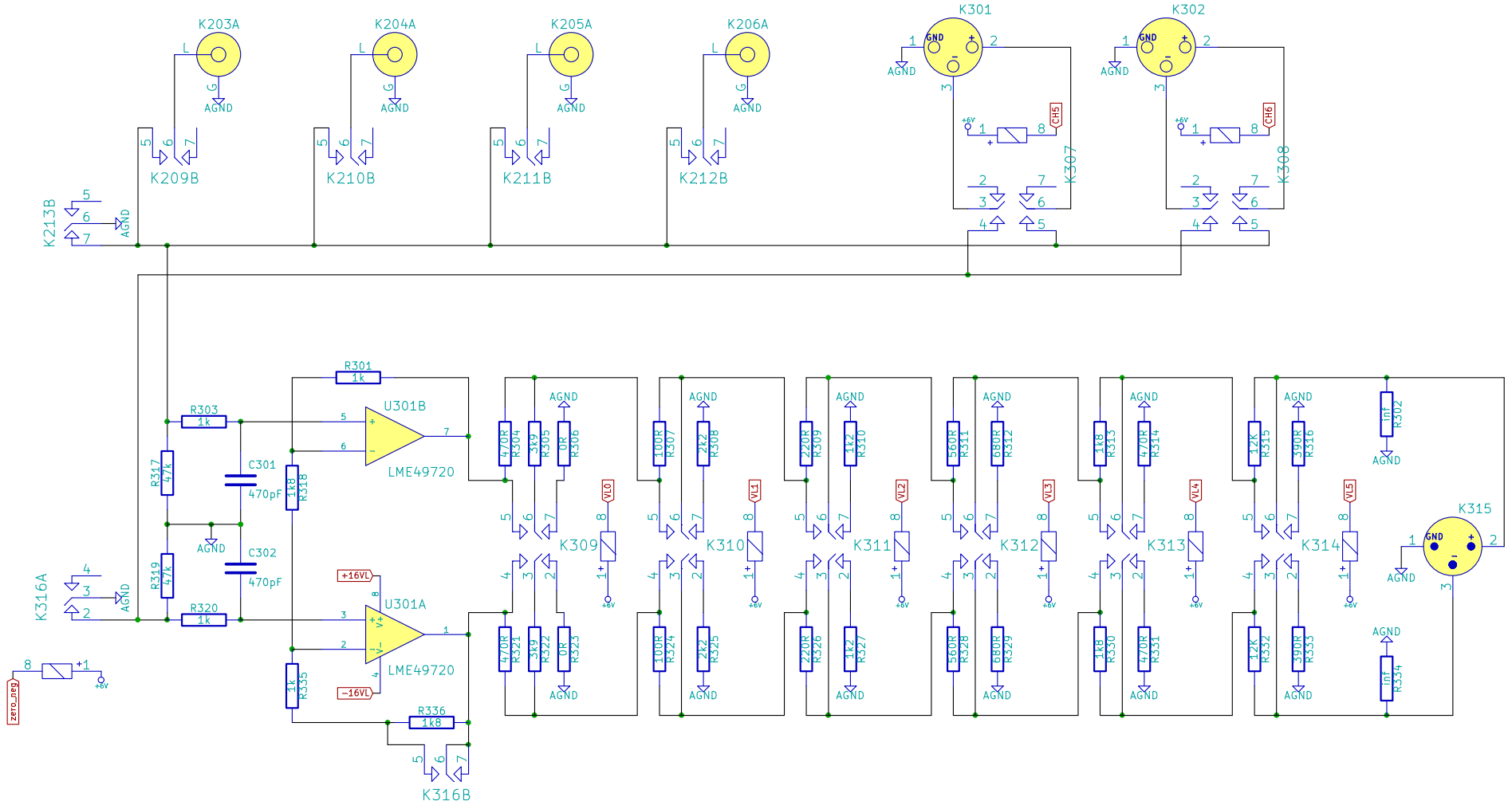


File: audio−right.sch		
Sheet: /AudioRight/		
Title:		
Size: A4	Date: 24 feb 2013	**Rev:**
KiCad E.D.A. eeschema (2012−01−19 BZR 3256)−stable		Id: 2/5



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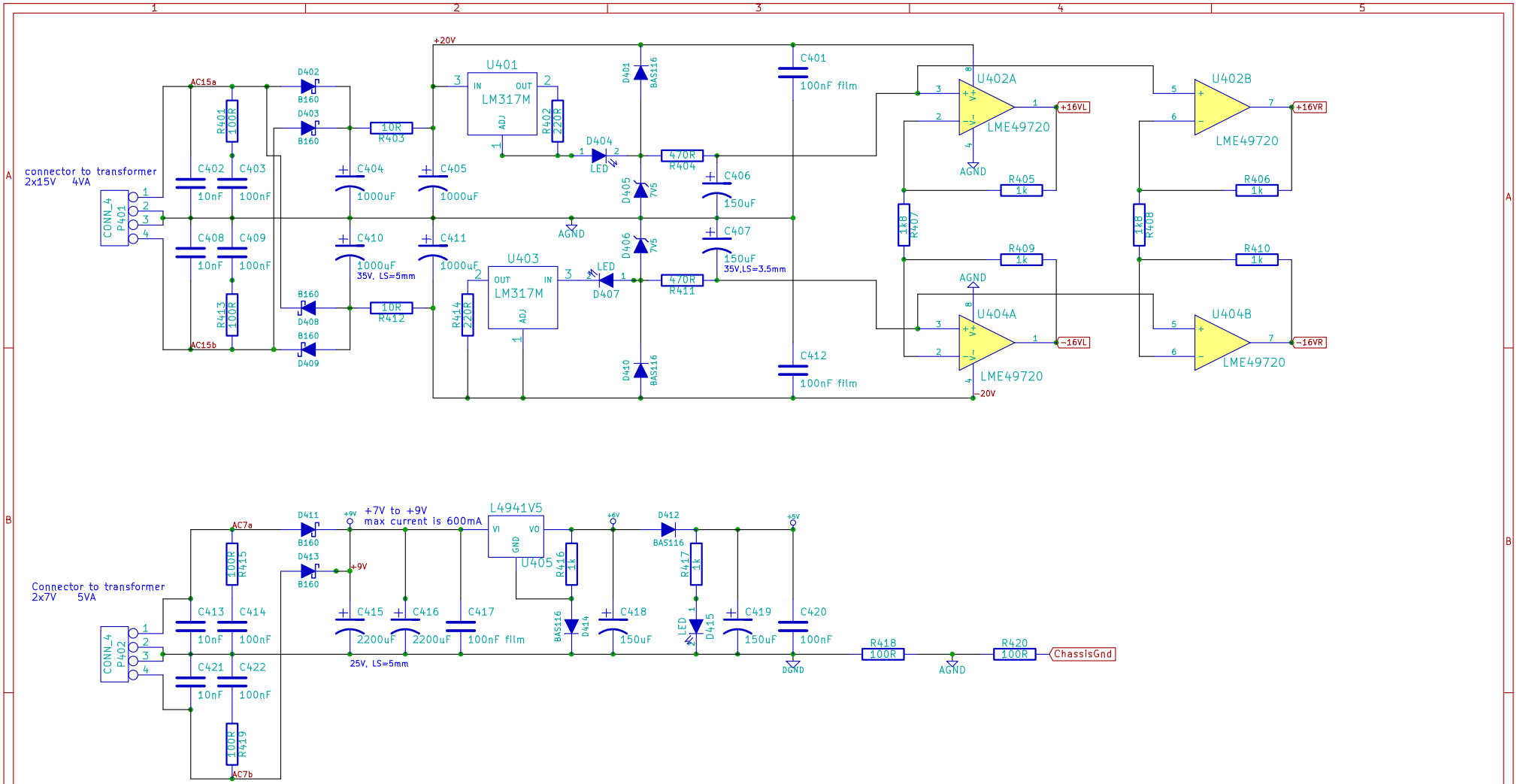
Title:		
Size: A4	Date: 24 feb 2013	**Rev:**
KiCad E.D.A.	eeschema (2012-01-19 BZR 3256)-stable	Id: 3/5



File: power.sch		
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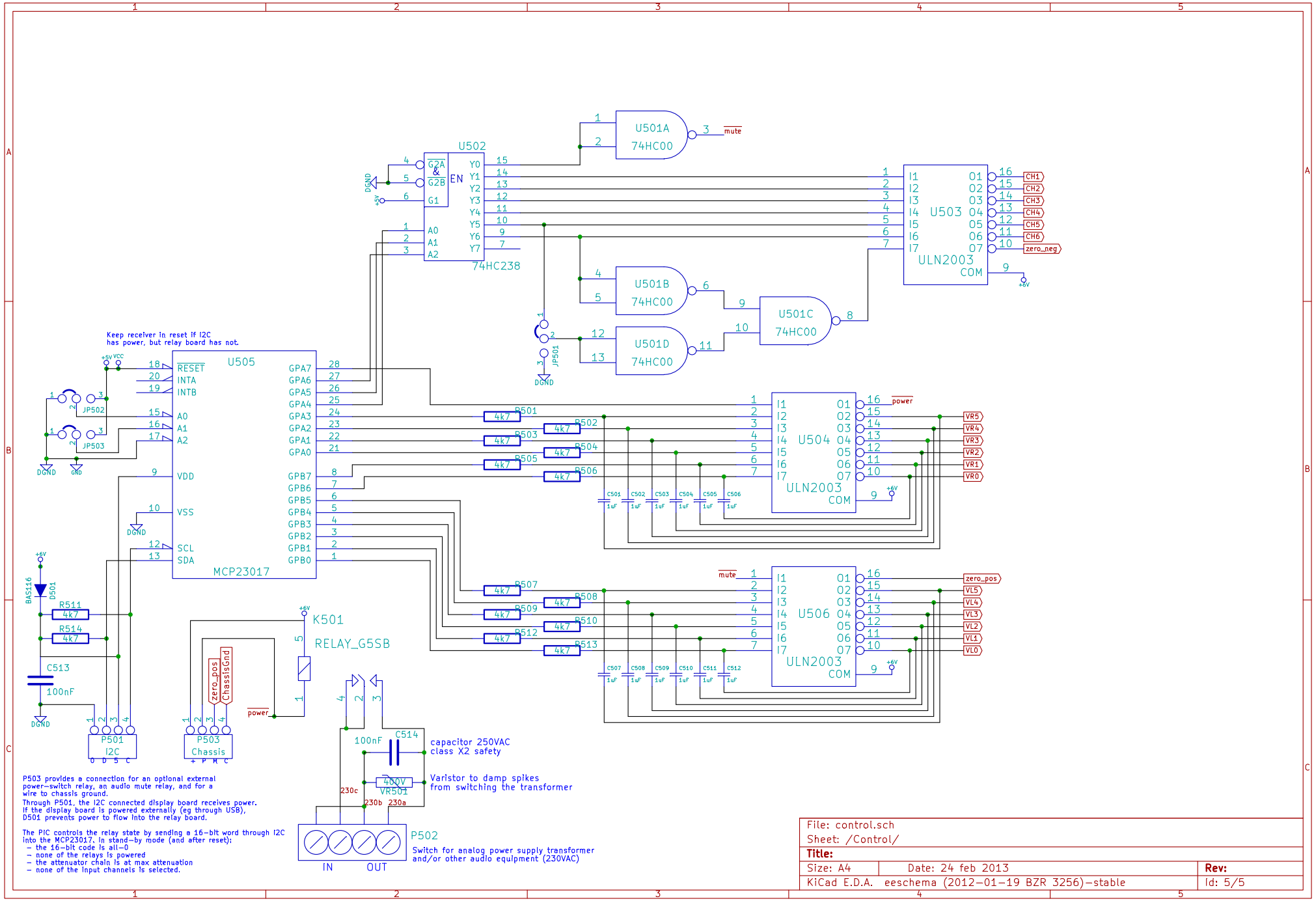
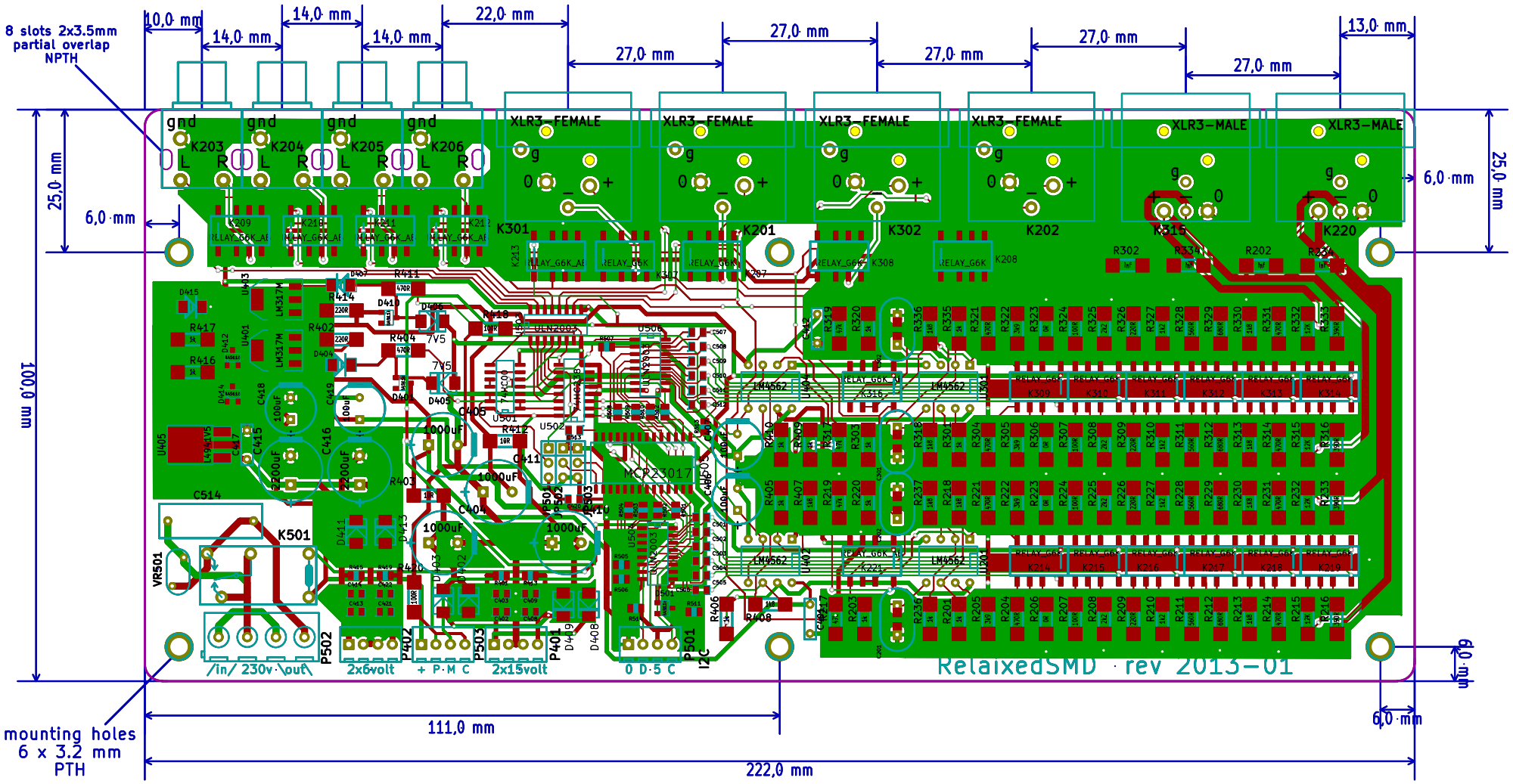
Size: A4	Date: 24 feb 2013	**Rev:**
KiCad E.D.A.	eeschema (2012-01-19 BZR 3256)-stable	Id: 4/5


U501A
74HC00
U501B
74HC00
U501C
74HC00
U501D
74HC00
U502
74HC238
U503
ULN2003
U504
ULN2003
U505
MCP23017
U506
ULN2003
JP501
JP502
JP503
K501
RELAY_G5SB
P501
I2C
P503
Chassis
P502
IN
OUT
mute
power
zero_pos
zero_neg
CH1
CH2
CH3
CH4
CH5
CH6
VR5
VR4
VR3
VR2
VR1
VR0
VL5
VL4
VL3
VL2
VL1
VL0
Keep receiver in reset if I2C has power, but relay board has not.
capacitor 250VAC class X2 safety
Varistor to damp spikes from switching the transformer
Switch for analog power supply transformer and/or other audio equipment (230VAC)
P503 provides a connection for an optional external power-switch relay, an audio mute relay, and for a wire to chassis ground.
Through P501, the I2C connected display board receives power.
If the display board is powered externally (eg through USB), D501 prevents power to flow into the relay board.
The PIC controls the relay state by sending a 16-bit word through I2C into the MCP23017. In stand-by mode (and after reset):
– the 16-bit code is all-0
– none of the relays is powered
– the attenuator chain is at max attenuation
– none of the input channels is selected.
File: control.sch
Sheet: /Control/
Title:
Size: A4
Date: 24 feb 2013
Rev:
KiCad E.D.A. eeschema (2012-01-19 BZR 3256)-stable
Id: 5/5



Jos van Eijndhoven		
File: toppage.brd		
Sheet: 1/1		
Title: Relaixed2smd		
Size: A4	Date: 13 jan 2013	Rev:
KiCad E.D.A. pcbnew (2012-01-19 BZR 3256)-stable		Id: 1/1